

This logic diagram is furnished for test and maintenance purposes only. Proprietary circuits are shown and should be treated accordingly.

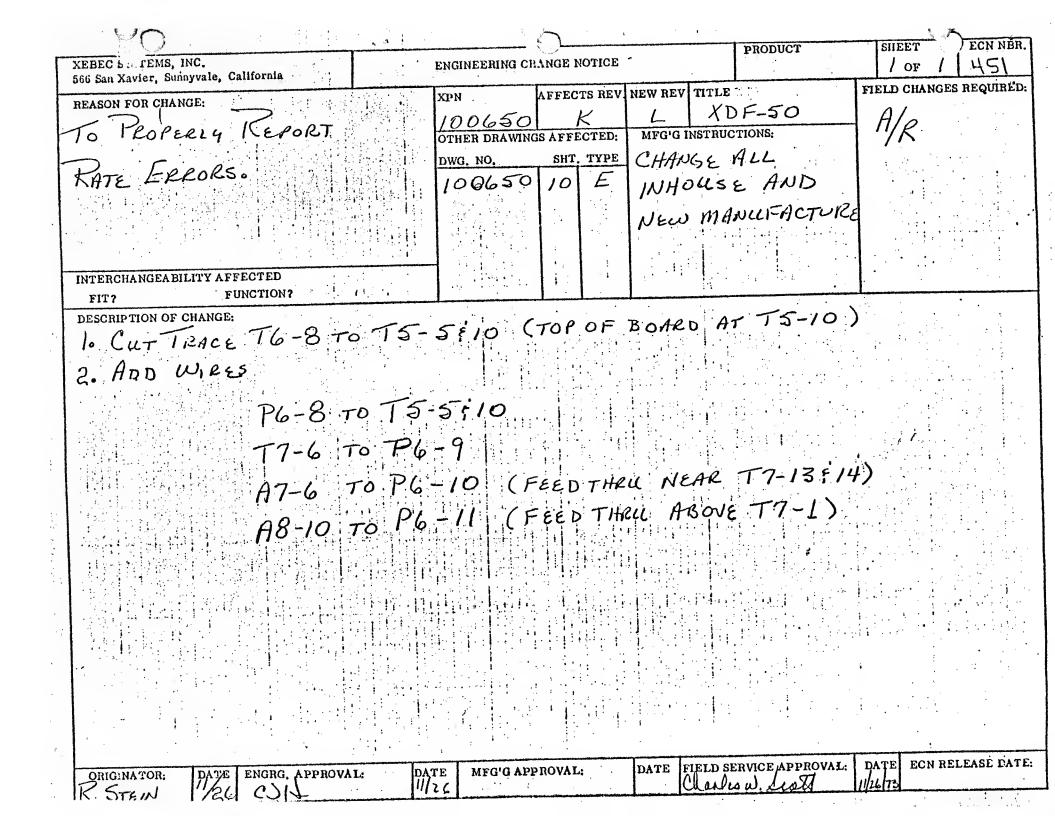
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XEBEC SYSTEMS INC

SCALE: NONE APPROVED BY: DRAWN: S.V.
DATE: 9-26-72 REVISED:

XDF-50

22 0F22 100650-E



NEEPC SYSTEMS, INC.		l poorion	SHEDY FEOTILES.
566 San Xavier, Sugayvale, California	ENGINEERING CHANGE NOTICE	XDF.50	SHEDY ECTIVES.
REASON FOR CHANGE:	XPN .FFECTS REV		Field Changes dequired.
Change lamp driver to 107438	OTHER DRAWINGS AFFECTED:	W XDF.20	
	DWG, NO. SHT. TYPE	MFG'G INSTRUCTIONS:	1/2
	100670-E 19 logic	All new production	
	11 280		
· 為此的名式。另一位, "是不能是我们的一个人,我们就是一个人。"			
	assy, Rur. M		
INTERCHANGEABILITY AFFECTED FIT? NO FUNCTION? No	ARTWORK PLUTE		
DESCRIPTION OF CHANGE:		•	
1. 100 to motal 10 7438 a	t location. Fl	DAFTING!	
2. Cut the following traces:		4. On ne	y taping: add
P1-4 (comp side)			, •
PI-6 (comp side)		150 x	redistor to GED
PI-6 (ckt =ide)		on P1	-3,6,8,11
P1-8 (cktside)			1
PI-10 (compside)			
0.0010.5			
s. Add the following wires.			*
81-9 to 61-10			
P1-4 to P1-2			
P1-2 to FT off R8-8			
P1-6 to FT 066 P1-4			
Pi-8 to FT 8 P1-10			***************************************
			•
			*
SRIGINATOR: DATE ENCAG APPROVAL: DA	TE MEG'G APPEOVAL:	DATE FIELD SERVICE APPROVAL:	DATE ECH RELEASE DATE:

XEBEC SYSTEMS, INC. 566 San Xavier, Sunnyvale, California	ENGINEERING C	IANGE NOTICE		PRODUCT	SHEET	ECN NBR
REASON FOR CHANGE:	ХРИ	AFFECTS REV	NEW REV	TITLE : :	FIELD CHANGE	S REQUIRED
TO INCORPORATE MODS, 100811	100650	M	\sim	FORMATTER	AIR	
	OTHER DRAWING	SS AFFECTED:	MFG'G	INSTRUCTIONS:	7 ''/	
100812, 100905 ON NEXT BUY	DWG. NO.	SHT. TYPE	Ale.	by test.	=	
OF BOARDS.	100650	rogic	,	1		
	100651	ARTHORK	-	, ma	, the second second	
1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1	100650	REVI.E.				
	100630	1 1231.				
INTERCHANGEABILITY AFFECTED					1:1	
FUNCTION?		<u> </u>			<u> </u>	
DESCRIPTION OF CHANGE:						•
1. TO CONVERT LLEGAL ADDRESS Pulses	to A LEVE	NEEDED	FOR :	STATUS REGISTER.	/ e	
4) Remove trace B2-15 to D3-9,	N7-4, 51-	11. (Lea	ve BZ	-15 connected to	14-2)	4
b) Remove trace D3-8 to F11-4.	^,					
		00			4 1 1 1 m	= 1
C) ADD 'D' FLIPFLOP (SN7474)	T LOCATIO	$j \in I$				
e) ADOTRACE B2-15 to F1-10	•			WAS		•
f) ADD TRACE K2-10 to F1-11.			B2-	15 9 0308		11-4
9) ADD TRACE D3-4 to F1-13.			J4-2		•	7-4
h) ADD TRACE FI-8 to N7-4,	1 '				51	1-11 1-2
i) ADD TRACE FI-9 to FII-H.			• • • • • • • • • • • • • • • • • • •	SHOULD B		
J) ADD TRACE GND to FI-7, FI	-12.		132-15 14-28	10		4-2
K) ADD TRACE VCC to FL-14.			. 40	12 610 9	F	11-4
			K2-10	, 11		
				F1 F1 7474 8		
			•	7/3		7-4 1-11
			D3-4			-
ORIGINATOR: DATE ENGRG, APPROVAL: DATE	MFG'G APPR	DVAT. I	Ame lere	ET D CEDUICE ADDOMA		
R. STEW 1/2 CAMEUN 1/4.	MEG G APPR	AUT:	ATE FIE	ELD SERVICE APPROVAL:	DATE ECN RELE	ASE DATE:

XEBEC SYSTEMS, INC. 566 San Xavier, Sunnyvale, California	ENGINEERING CI	ANGE NOTICE		PRODUCT	SHEET 2 of 4	ECN NER.
REASON FOR CHANGE:	. хэм 100650	AFFECTS REV.	N -		FIELD CHANGES	REQUIRED.
	OTHER DRAWING	SAFFECTED: SHT. TYPE	MFG'G I	nstructions:	`	
				=		
INTERCHANGEABILITY AFFECTED FIT? FUNCTION?						
DESCRIPTION OF CHANGE: 2. To shorten sector pulse to						•
a) Remove trace C3-9 to K5 b) Add one-shot (9601) AT 1						
c) Add trace C3-9 to L2-1 d) Add trace L2-8 to K5-13 e) Add trace L2-13 to 15K	3					
f) Add trace GND to 42-7. g) Add trace VCC to 42-2,	12-3, L2-4, L2					
C3-9 K5-13	C3-9	5.	FOULD CC 10 20 2100 14 9601	15k 	S-13	
ORIGINATOR: DATE ENGRG, APPROVAL:	DATE MFG'G APP	ROVAL:	DATE FI	ELD SERVICE APPROVAL:	DATE ECN REL	EASE DATE

REASON FOR CHANGE: XPN AFFECTS REV NEW REV TITLE FIELD CHANGES REV NOW REV TITLE TOUGHS OF THE DRAWINGS AFFECTED. DWG, NO. SHT. TYPE INTERCHANGEABILITY AFFECTED FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPHRATE HEAD PLATTER AND SECK COMPLETE INVERSION. a) Change M3 to 110 pin configuration.	equired:
OTHER DRAWINGS AFFECTED; MFG'G INSTRUCTIONS: DWG, NO. SHT. TYPE INTERCHANGEABILITY AFFECTED FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPHRATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	
INTERCHANGEABILITY AFFECTED FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPHRATE HEAD PLATTER AND SEZK COMPLETE INVERSION.	
INTERCHANGEABILITY AFFECTED FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPHRATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	
FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPARATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	
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FIT? FUNCTION? DESCRIPTION OF CHANGE: 3) TO Allow SEPARATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	•
DESCRIPTION OF CHANGE: 3) TO Allow SEPHRATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	
3) TO AllOW SEPHRATE HEAD PLATTER AND SEEK COMPLETE INVERSION.	
b) Renumber per diagram	
c) Remove trace M3-14 to K8-4. (LEAVE M3-14 connected to K8-2, K8-10, N3-11)	
d) Add trace M3-16 to K8-4, N3-6.	
NEW	
NOTE: OLD M3-1 thru 7 becomes NEW M3-	2#1.8,
$\frac{1}{3}$	1 1
010 m3-8 three 14 becomes New M3-9+	hyu 15
7 8 3 4 4 4 5 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	
ORIGINATOR: DATE ENGRG, APPROVAL: DATE MFG'G APPROVAL: DATE FIELD SERVICE APPROVAL: DATE ECH RELEAS	

XEBEC SYSTEMS, INC.	EN	NGINEERING CH	ANGE NOTICE			PRODUCT		HEET	ECN MER.
566 San Xavier, Sunnyvale, California	· - 1	nancje na zavije governo i do nancje na zavije na	A TO TO COMO TO TO TO	1	l			4 of 4	467 +
REASON FOR CHANGE:			AFFECTS REV	NEW RE	VITTLE	· ·	FIEL	D CHANGES	REQUIRED;
	<u> </u>	100650			3 INSTRUC	PTONG.	-		
	- 1			MFG	3 INST NUC	TIONS:			
	. 1	WG. NO.	· SHT. TYPE						
		·		1		•	1.	;	•
								•	
								•	
		:			:				
INTERCHANGEABILITY AFFECTED									
FIT? FUNCTION?			<u> </u>	 	· · · · · · · · · · · · · · · · · · ·			· · · · · · · · · · · · · · · · · · ·	
DESCRIPTION OF CHANGE:									
4. To shorten Read clocks and	rea	ed clock	delaye	d fo	ov pv	oper oper	Aio	9	
2 400 rpmo	-						•		.:
	• •		T		*	•			
a) change resistor @ K7-1	3 .	from 15	4: to 10	k.	1.				• .
b) change resistor @ 19-1	ر غ	ر ر		1.1.				• ;	
70) 1	- Trom [5]	. 40 9·	I K					
c) CHANGE RESISTOR @ J8-1	3 f	rom 15K	to 1.0	k.				• •	
5. To correct melfunction circuit	<i>t</i>								
								0	*
a) Remove trace A 3-1 to L	4-13	3. (LEAL	L A3-1 0	Lonne	sted to	· 12-12)	. •	00	
b) Add trace B4-6 to L4-	12						•	*	
37.60	، ،		± ' '	:			i		<u> </u>
				1					
							•		: 4.
						i	;	:	
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					. 1		: •		
							•	•	: 0
				· .			1		
ORIGINATOR: DATE ENGRG. APPROVAL:	ATE	MFG'G APPR	DVÅT:	DATE	field ser	VICE APPROVAL:	DATE	ECN RELE	ASE DATE:

XEBEC STATEMS, INC. 566 San Xavier, Suányvale, California	ENGINEERING CI	IANGE NOTICE	,	PRODUCT XDF-50	SHEET \ OF \	ECH NER.
REASON FOR CHANGE: TO IMPROVE PRODUCT	100620	AFFECTS REV.	P	XDF-50	FIELD CHANGES	REQUIRED:
CONSISTENCY WITH VARYING VENDORS.	DWG. NO.	SHT, TYPE	ИОДІ	NSTRUCTIONS: FY ALL P.C. BDS	* \	
JENDOKS.	100650	I ASSY	1107 12	ST, STOCK &		
INTERCHANGEA BILITY AFFECTED		•				*
FIT? FUNCTION?				•	•	

DESCRIPTION OF CHANGE:

- I. CHANGE VALUE OF TIMING RESISTOR ON H8-13 TO BE 5.1 K
- 2. ADD 27 PF. CAP. BETWEEN HB-11 & HB-13.
- 3. CUT TRACE T3-2 TO T3-13.
- H. ADD WIRE T3-2 TO T3-1.